



# UTT15P06

Power MOSFET

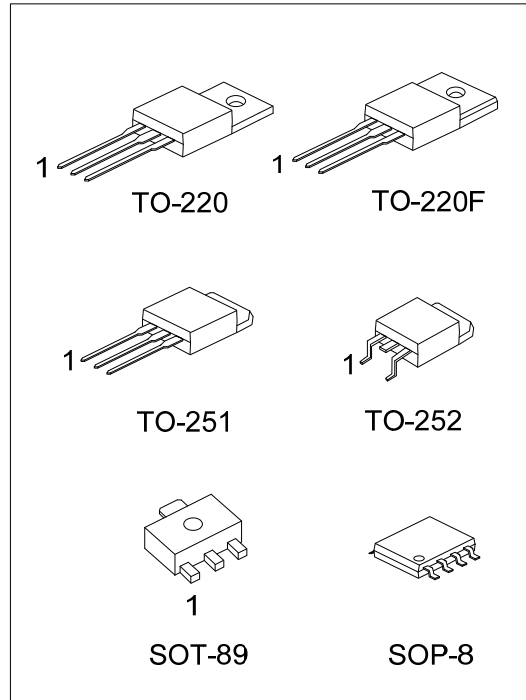
## -15A, -60V P-CHANNEL POWER MOSFET

### DESCRIPTION

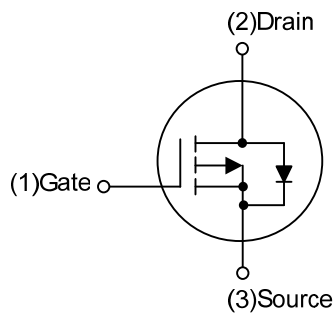
The UTC **UTT15P06** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed, cost-effectiveness and minimum on-state resistance. It can also withstand high energy in the avalanche.

### FEATURES

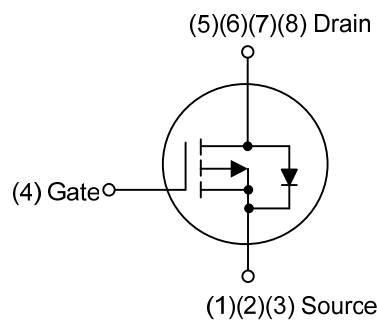
- \*  $R_{DS(ON)} \leq 75 \text{ m}\Omega$  @  $V_{GS}=-10\text{V}$ ,  $I_D=-15\text{A}$
- \*  $R_{DS(ON)} \leq 120 \text{ m}\Omega$  @  $V_{GS}=-4.5\text{V}$ ,  $I_D=-7.5\text{A}$
- \* High Switching Speed



### SYMBOL



SOT-89 / TO-220  
TO-220F / TO-251 / TO-252

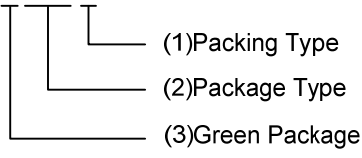


SOP-8

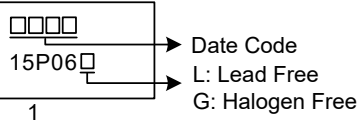
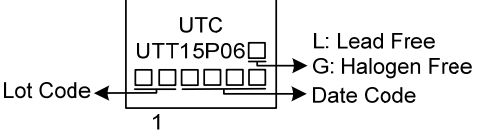
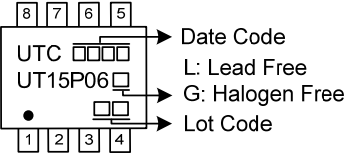
### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UTT15P06L-AB3-R	UTT15P06G-AB3-R	SOT-89	G	D	S	-	-	-	-	-	Tape Reel
UTT15P06L-TA3-T	UTT15P06G-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
UTT15P06L-TF3-T	UTT15P06G-TF3-T	TO-220F	G	D	S	-	-	-	-	-	Tube
UTT15P06L-TM3-T	UTT15P06G-TM3-T	TO-251	G	D	S	-	-	-	-	-	Tube
UTT15P06L-TN3-R	UTT15P06G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UTT15P06L-S08-R	UTT15P06G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UTT15P06G-AB3-R</p>  <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) R: Tape Reel, T: Tube (2) AB3: SOT-89, TA3: TO-220, TF3: TO-220F, TM3: TO-251, TN3: TO-252, S08: SOP-8 (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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### MARKING

PACKAGE	MARKING
SOT-89	 <p>15P06 → Date Code L: Lead Free G: Halogen Free</p>
TO-220 / TO-220F TO-251 / TO-252	 <p>UTC UTT15P06 → L: Lead Free G: Halogen Free Date Code Lot Code</p>
SOP-8	 <p>UTC → Date Code UT15P06 → L: Lead Free G: Halogen Free Lot Code</p>

■ ABSOLUTE MAXIMUM RATINGS ( $T_c=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DS}$	-60	V
Gate-Source Voltage		$V_{GS}$	$\pm 25$	V
Drain Current	Continuous	$I_D$	-15	A
	Pulsed	$I_{DM}$	-30	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	16.2	mJ
Power Dissipation	SOT-89	$P_D$	4.3	W
	TO-220		79	W
	TO-220F		37	W
	TO-251/TO-252		39	W
	SOP-8		3	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L = 0.1\text{mH}$ ,  $I_{AS} = -18\text{A}$ ,  $V_{DD} = -30\text{V}$ ,  $R_G = 25\ \Omega$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient (Steady state)	SOT-89	$\theta_{JA}$	180	$^\circ\text{C/W}$
	TO-220/TO-220F		62	$^\circ\text{C/W}$
	TO-251/TO-252		110	$^\circ\text{C/W}$
	SOT-8		125	$^\circ\text{C/W}$
Junction to Case	SOT-89	$\theta_{JC}$	29	$^\circ\text{C/W}$
	TO-220		1.58	$^\circ\text{C/W}$
	TO-220F		3.3	$^\circ\text{C/W}$
	TO-251/TO-252		3.2 (Note)	$^\circ\text{C/W}$
	SOT-8		41.67 (Note)	$^\circ\text{C/W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

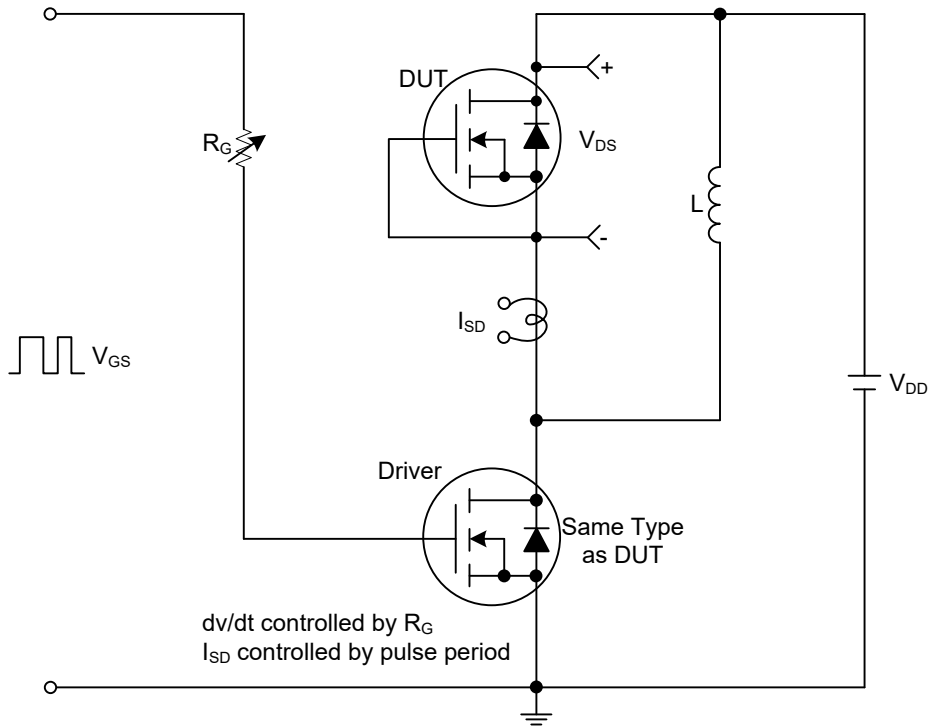
■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =-250μA, V <sub>GS</sub> =0V	-60			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V			-1	μA
Gate-Source Leakage Current	Forward	I <sub>GSS</sub> V <sub>GS</sub> =+25V, V <sub>DS</sub> =0V V <sub>GS</sub> =-25V, V <sub>DS</sub> =0V			+100	nA
	Reverse				-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-15A			75	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-7.5A			120	mΩ
<b>DYNAMIC PARAMETERS (Note 2)</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-25V, f=1.0MHz		1086		pF
Output Capacitance	C <sub>OSS</sub>			97		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			61.6		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	Q <sub>G</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-15A I <sub>G</sub> =-1mA (Note 1, 2)		22		nC
Gate to Source Charge	Q <sub>GS</sub>			5		nC
Gate to Drain Charge	Q <sub>GD</sub>			3		nC
Turn-ON Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> =-30V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-15A, R <sub>G</sub> =3.3Ω (Note 1, 2)		6		ns
Rise Time	t <sub>R</sub>			16		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			30		ns
Fall-Time	t <sub>F</sub>			20		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>C</sub>=25°C) (Note 2)</b>						
Maximum Body-Diode Continuous Current	I <sub>S</sub>				-15	A
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				-30	A
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> =-15A, V <sub>GS</sub> =0V (Note 1)		-1.0	-1.5	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>S</sub> =-15A, V <sub>GS</sub> =0V,		40		ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	dI <sub>F</sub> /dt=100A/μs (Note 2)		35		nC

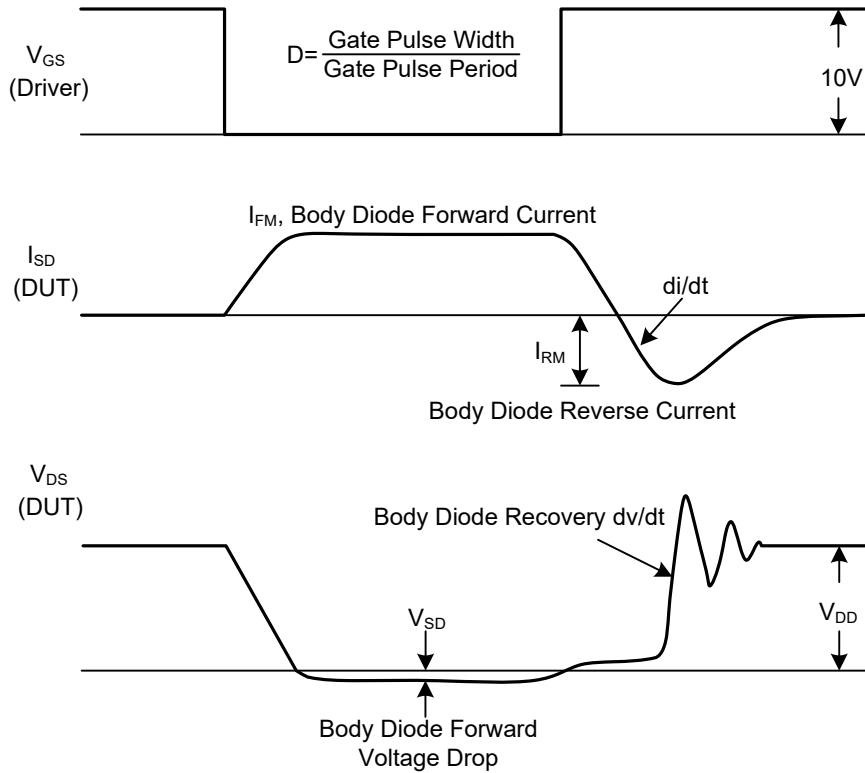
Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

TEST CIRCUITS AND WAVEFORMS



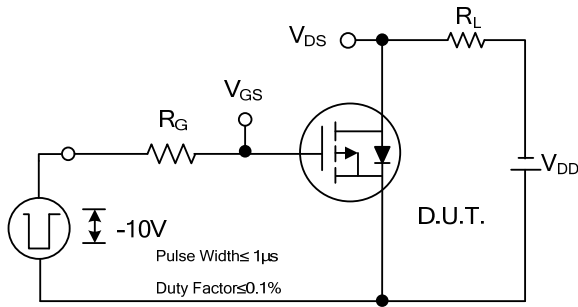
Peak Diode Recovery dv/dt Test Circuit



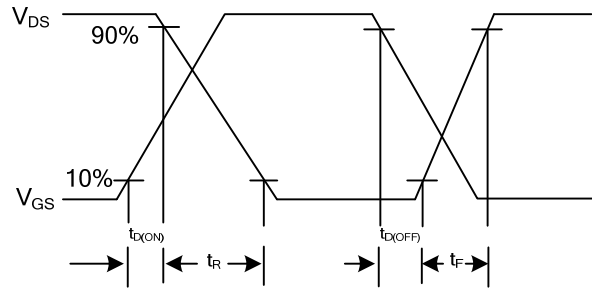
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

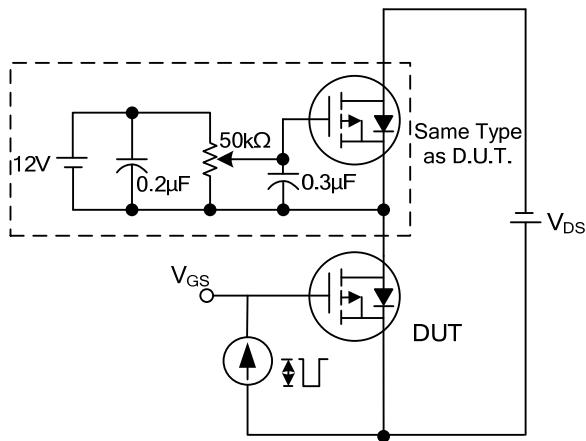
TEST CIRCUITS AND WAVEFORMS



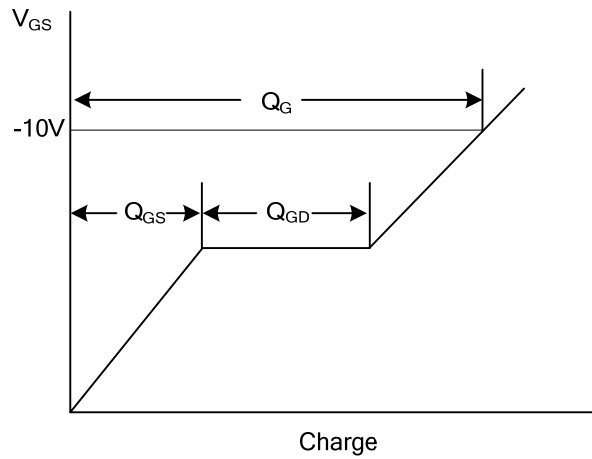
Switching Test Circuit



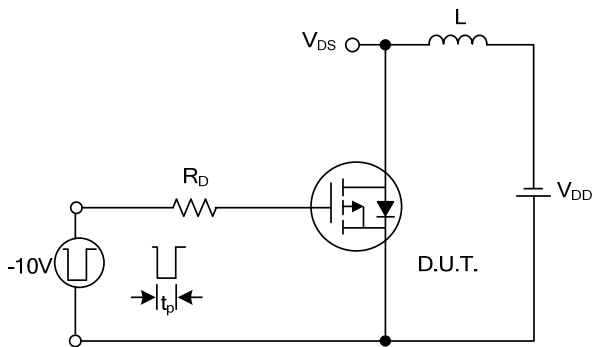
Switching Waveforms



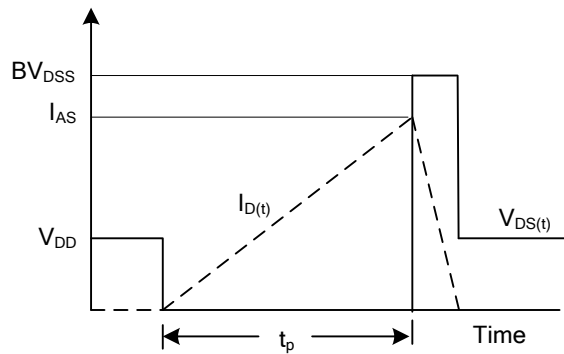
Gate Charge Test Circuit



Gate Charge Waveform

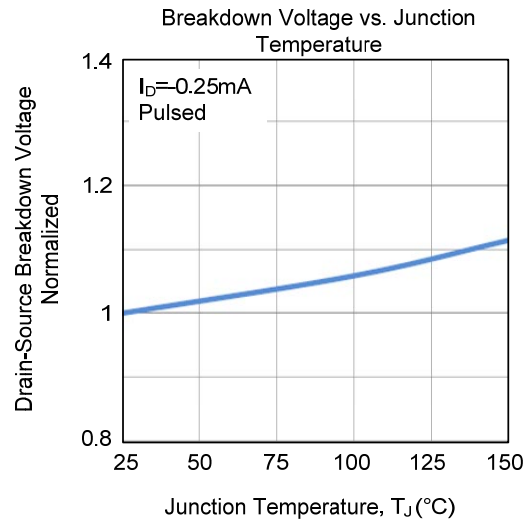
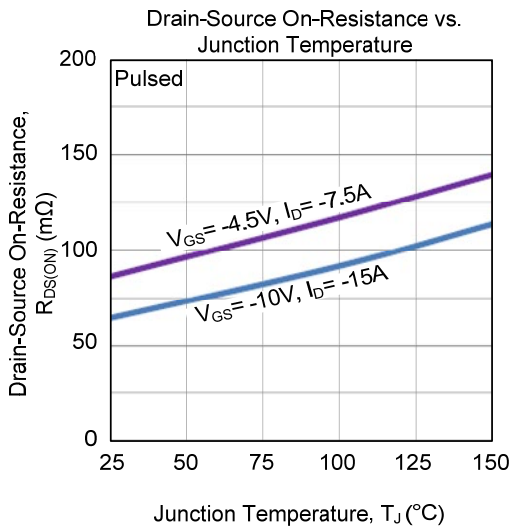
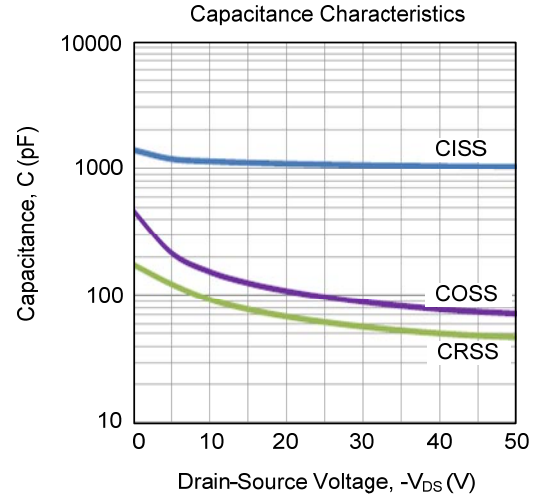
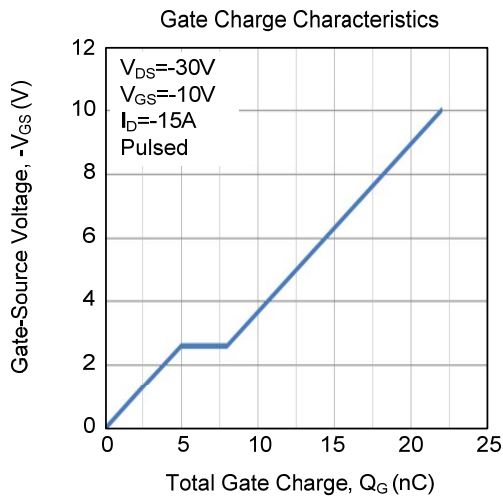
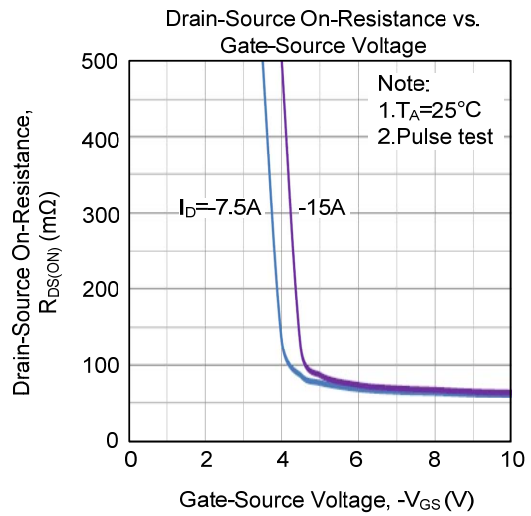
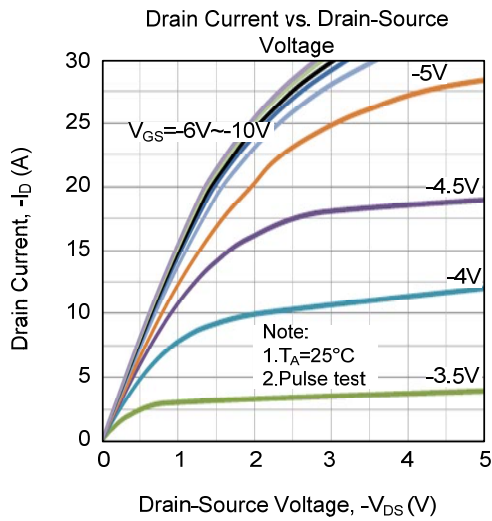


Unclamped Inductive Switching Test Circuit

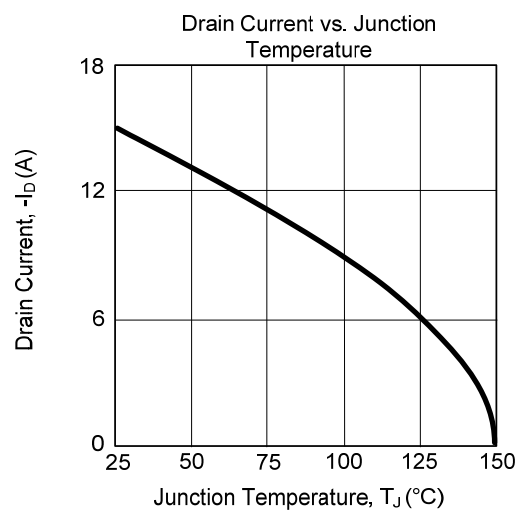
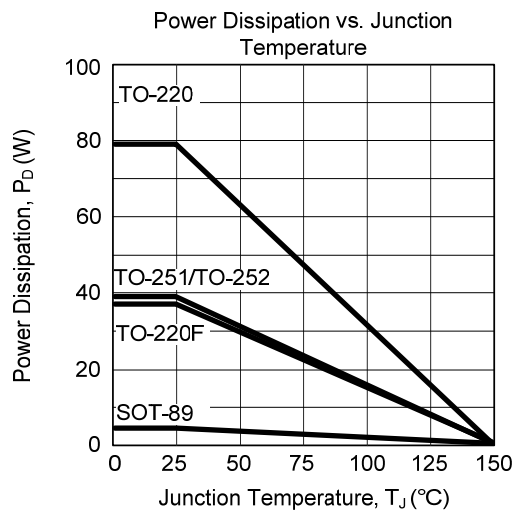
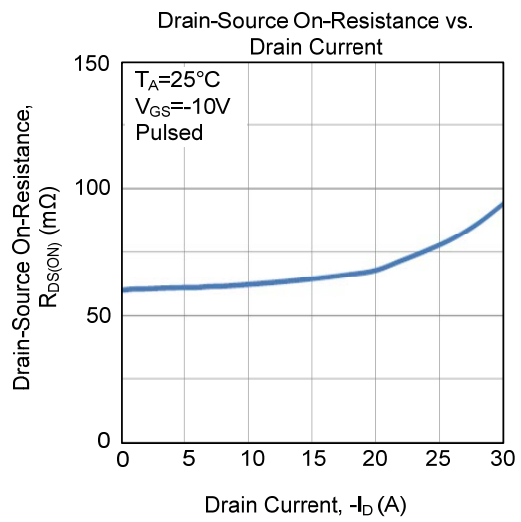
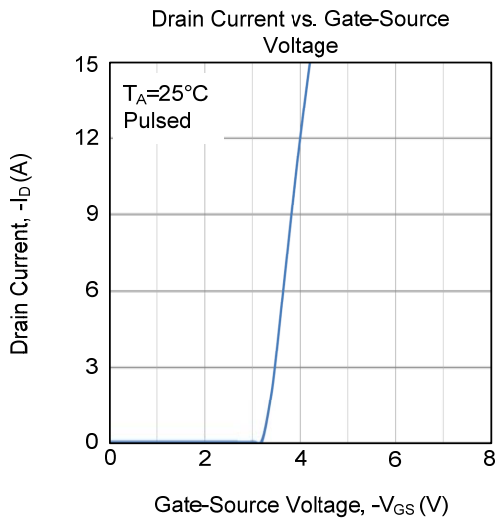
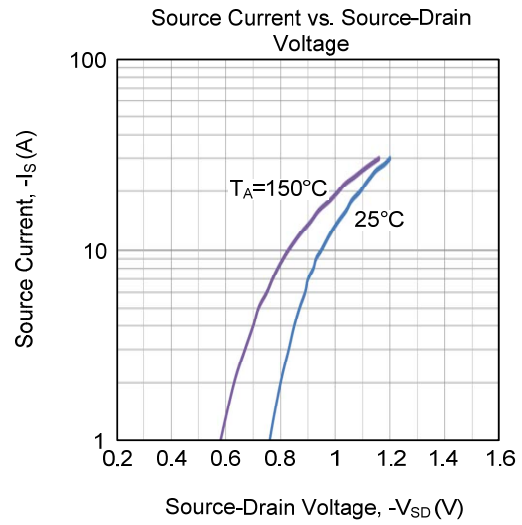
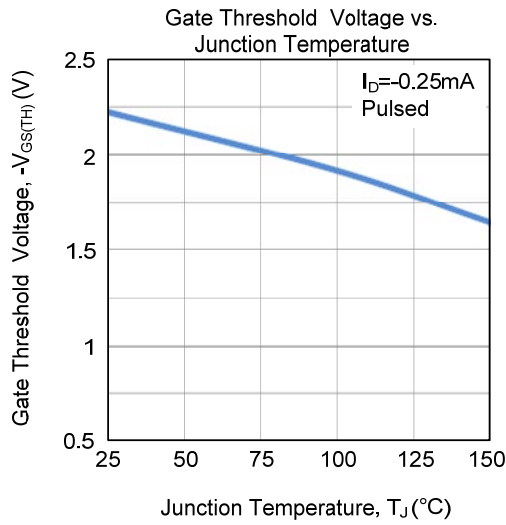


Unclamped Inductive Switching Waveforms

## TYPICAL CHARACTERISTICS

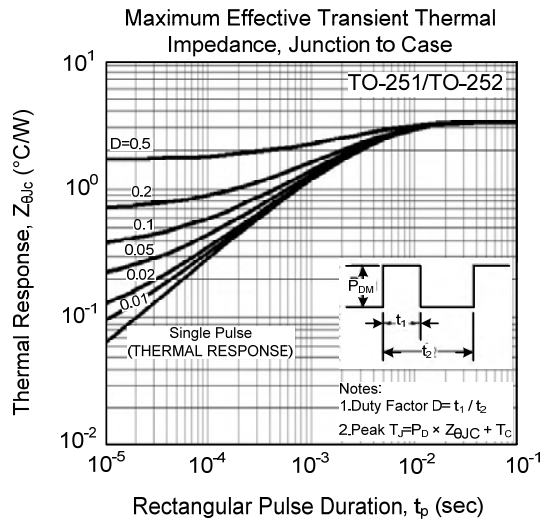
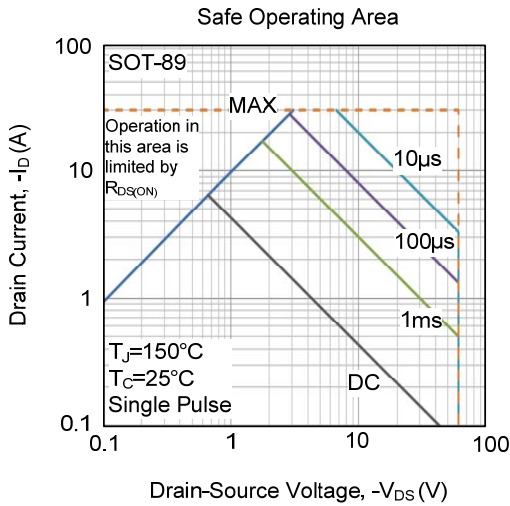
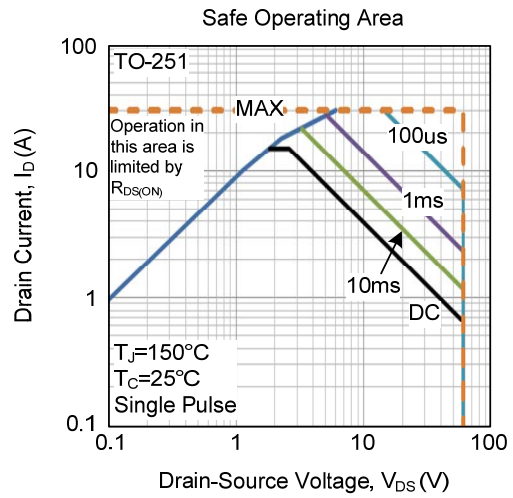
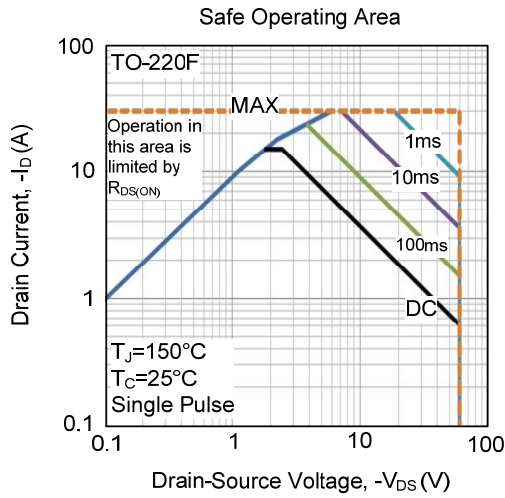


## TYPICAL CHARACTERISTICS (Cont.)





## TYPICAL CHARACTERISTICS (Cont.)



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