

# F4N60-ML

**Power MOSFET**

## 4.0A, 600V N-CHANNEL POWER MOSFET

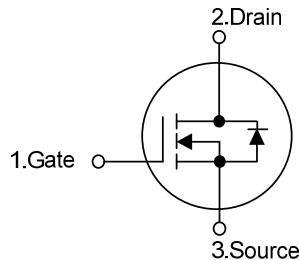
### ■ DESCRIPTION

The UTC **F4N60-ML** is a N-Channel enhancement mode silicon gate power MOSFET with Fast Body Diode, is designed high voltage, high speed power switching applications such, is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

### ■ FEATURES

- \*  $R_{DS(ON)} \leq 2.6 \Omega$  @  $V_{GS}=10V$ ,  $I_D=2.0A$
- \* Fast body diode MOSFET technology
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness

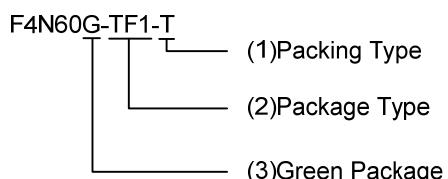
### ■ SYMBOL



### ■ ORDERING INFORMATION

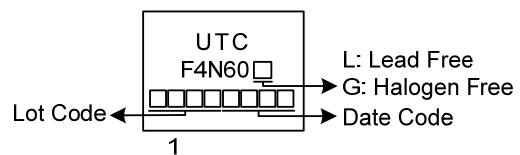
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
F4N60L-TF1-T	F4N60G-TF1-T	TO-220F1	G	D	S	Tube
F4N60L-TF2-T	F4N60G-TF2-T	TO-220F2	G	D	S	Tube
F4N60L-TM3-T	F4N60G-TM3-T	TO-251	G	D	S	Tube
F4N60L-TN3-R	F4N60G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



(1) T: Tube, R: Tape Reel  
 (2) TF1: TO-220F1, TF2: TO-220F2, TM3: TO-251  
 TN3: TO-252  
 (3) G: Halogen Free and Lead Free, L: Lead Free

## ■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	$V_{DSS}$	600	V	
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V	
Continuous Drain Current	$I_D$	4	A	
Pulsed Drain Current (Note 2)	$I_{DM}$	8	A	
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	mJ	
Peak Diode Recovery $dv/dt$ (Note 4)	$dv/dt$	7.2	V/ns	
Power Dissipation	TO-220F1/TO-220F2	$P_D$	32	W
	TO-251/TO-252		48	W
Junction Temperature	$T_J$	+150	$^\circ\text{C}$	
Storage Temperature	$T_{STG}$	-55 ~ +150	$^\circ\text{C}$	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L = 1.0\text{mH}$ ,  $I_{AS} = 6.7\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25 \Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 4.0\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	62.5	$^\circ\text{C/W}$
TO-251/TO-252		110	$^\circ\text{C/W}$
Junction to Case	$\theta_{JC}$	3.9	$^\circ\text{C/W}$
TO-251/TO-252		2.6 (Note)	$^\circ\text{C/W}$

Note: Device mounted on FR-4 substrate  $P_C$  board, 2oz copper, with 1inch square copper plate.

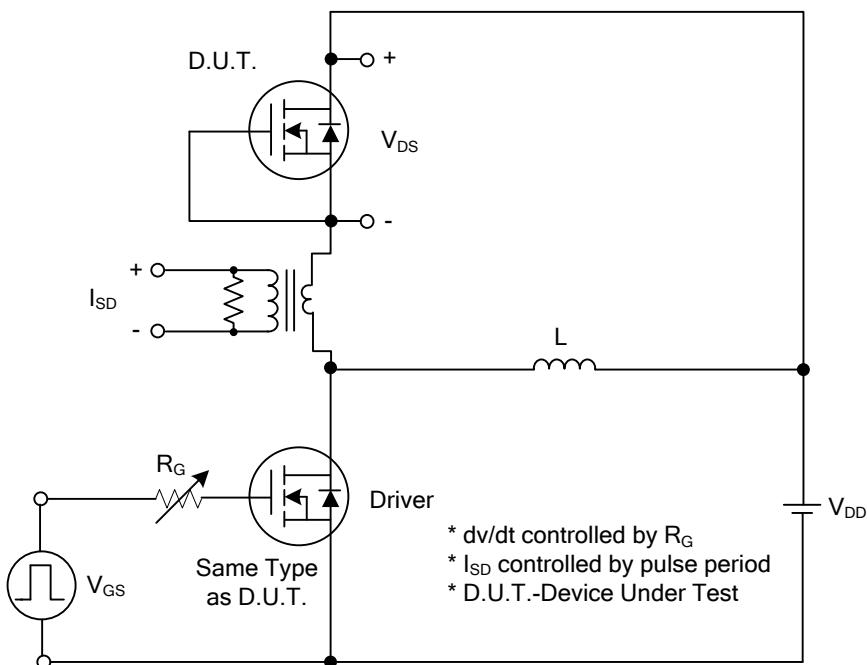
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	600			V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=600\text{V}, V_{\text{GS}}=0\text{V}$		10		$\mu\text{A}$
Gate- Source Leakage Current	Forward	$V_{\text{GS}}=30\text{V}, V_{\text{DS}}=0\text{V}$		100		nA
	Reverse	$V_{\text{GS}}=-30\text{V}, V_{\text{DS}}=0\text{V}$		-100		nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=2.0\text{A}$			2.6	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{\text{ISS}}$	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$		580		pF
Output Capacitance	$C_{\text{OSS}}$			53		pF
Reverse Transfer Capacitance	$C_{\text{RSS}}$			4		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$Q_G$	$V_{\text{DS}}=480\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=4\text{A}$ $I_G=1\text{mA}$ (Note 1, 2)		12.8		nC
Gate-Source Charge	$Q_{\text{GS}}$			4		nC
Gate-Drain Charge	$Q_{\text{GD}}$			2		nC
Turn-On Delay Time (Note 1)	$t_{\text{D}(\text{ON})}$	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=4\text{A}$ , $R_G=25\Omega$ (Note 1, 2)		5		ns
Turn-On Rise Time	$t_R$			16		ns
Turn-Off Delay Time	$t_{\text{D}(\text{OFF})}$			36		ns
Turn-Off Fall Time	$t_F$			26		ns
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
Maximum Body-Diode Continuous Current	$I_S$				4	A
Maximum Body-Diode Pulsed Current	$I_{\text{SM}}$				8	A
Drain-Source Diode Forward Voltage (Note 1)	$V_{\text{SD}}$	$I_S=4\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Reverse Recovery Time (Note 1)	$t_{\text{rr}}$	$I_S=4\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=100\text{A}/\mu\text{s}$		102		ns
Reverse Recovery Charge	$Q_{\text{rr}}$			580		nC

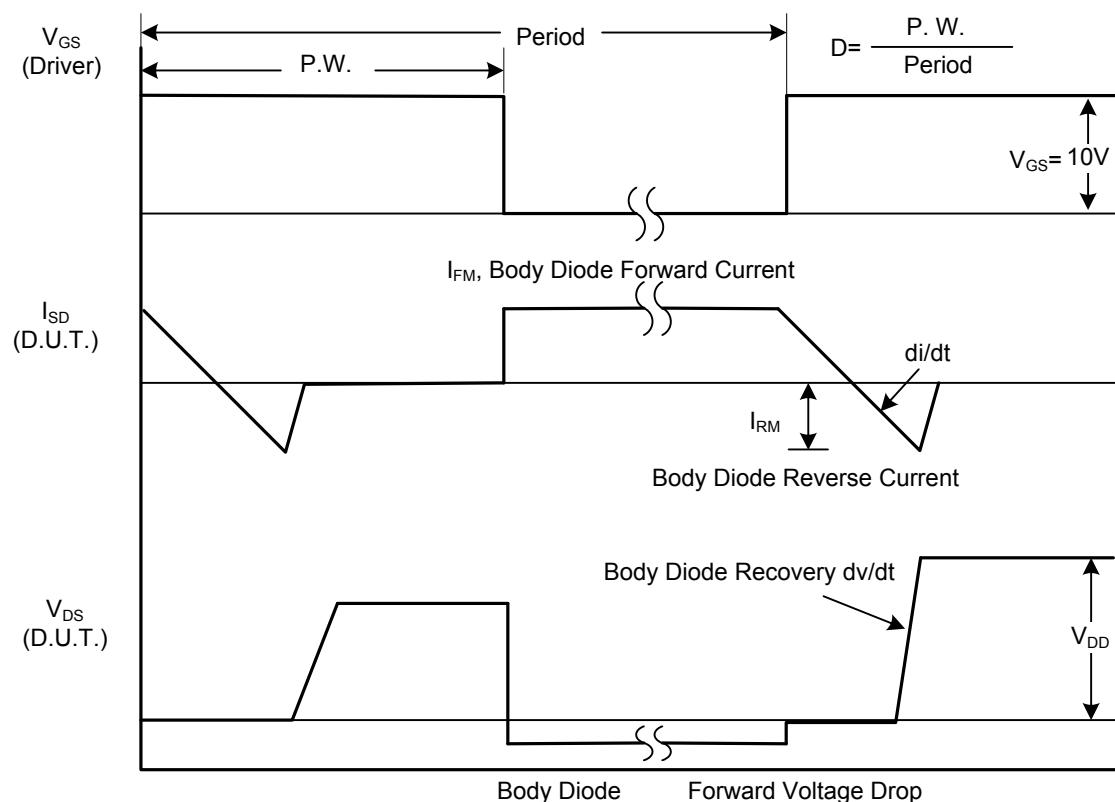
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

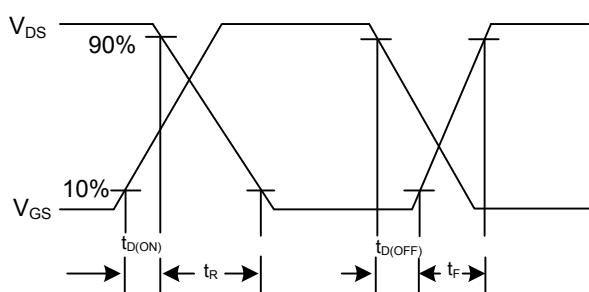
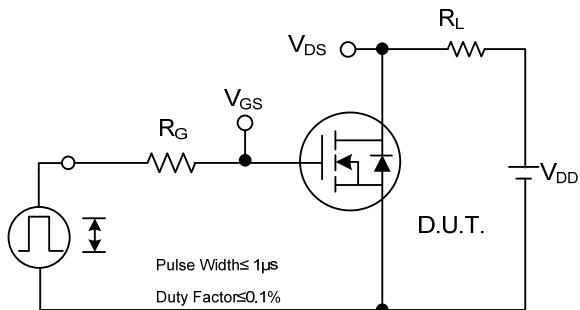


**Peak Diode Recovery dv/dt Test Circuit**



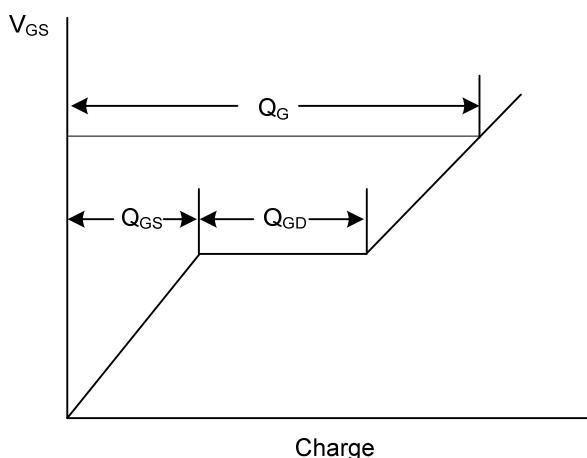
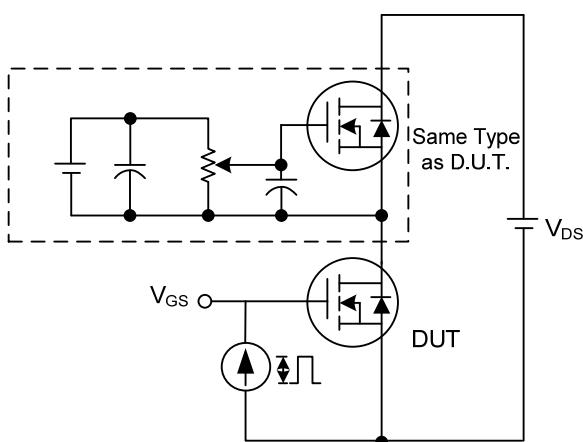
**Peak Diode Recovery dv/dt Waveforms**

### ■ TEST CIRCUITS AND WAVEFORMS



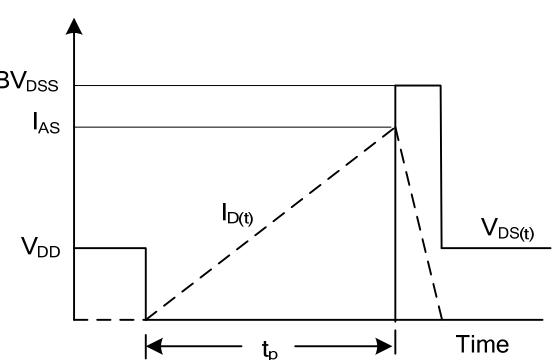
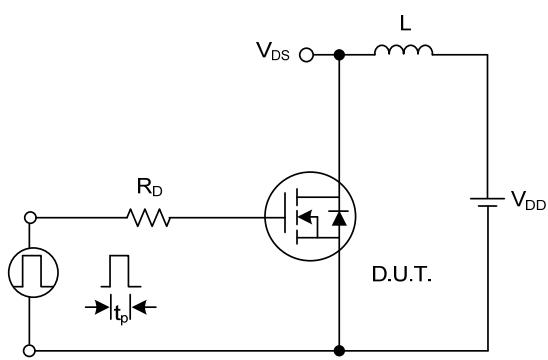
**Switching Test Circuit**

**Switching Waveforms**



**Gate Charge Test Circuit**

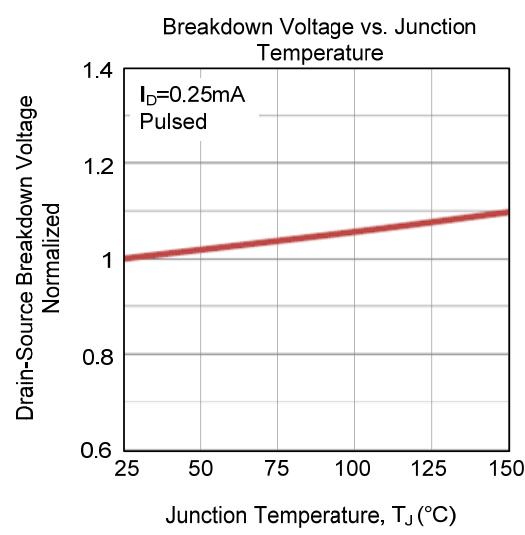
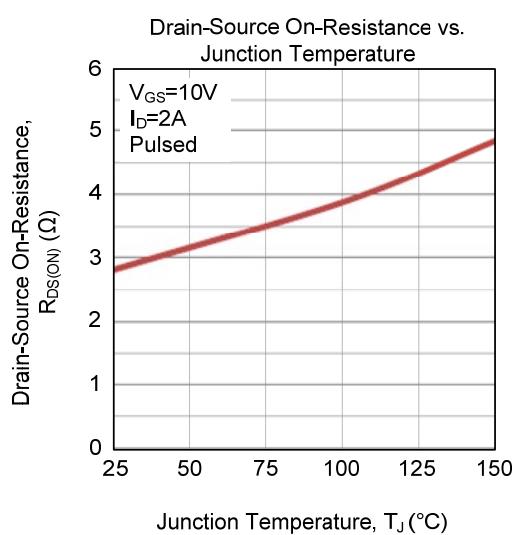
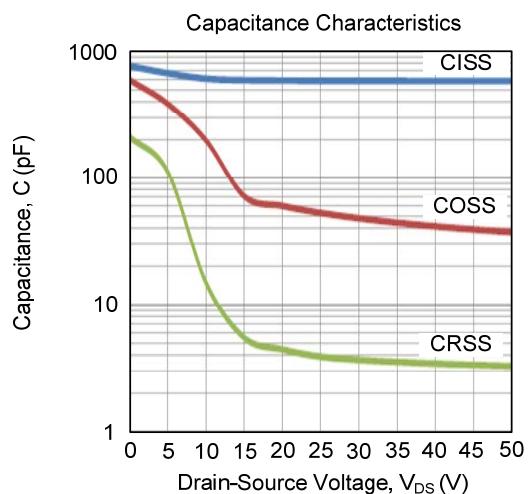
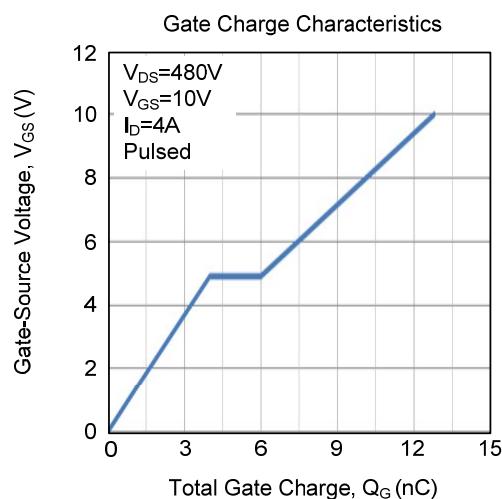
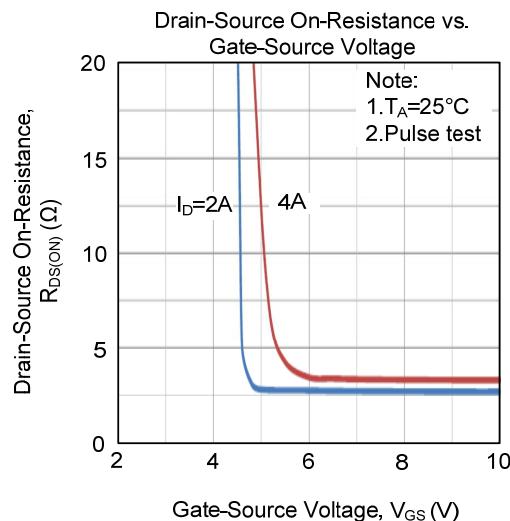
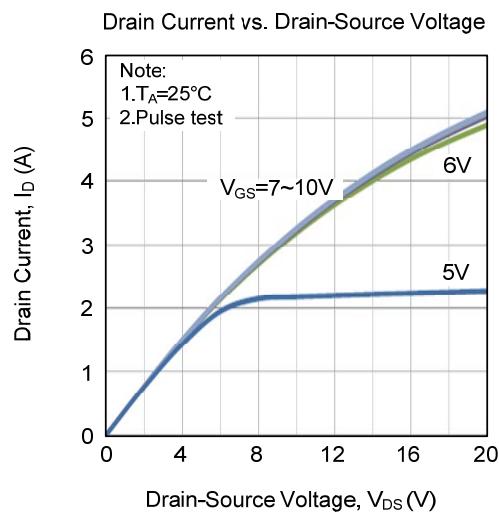
**Gate Charge Waveform**



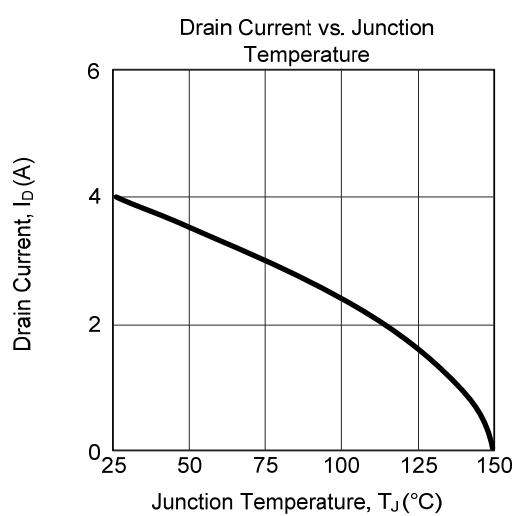
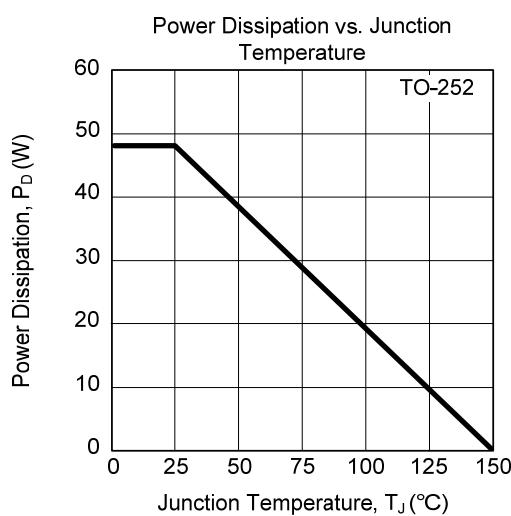
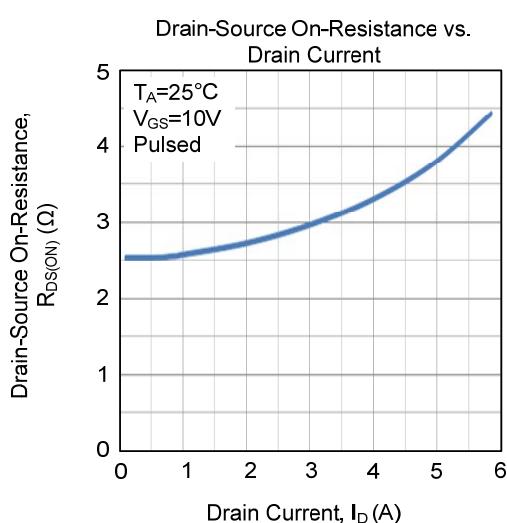
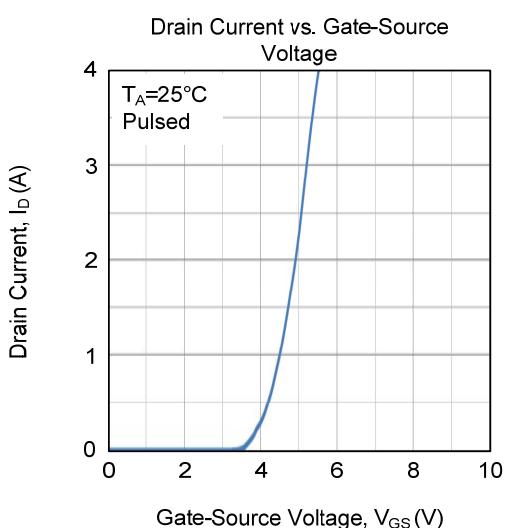
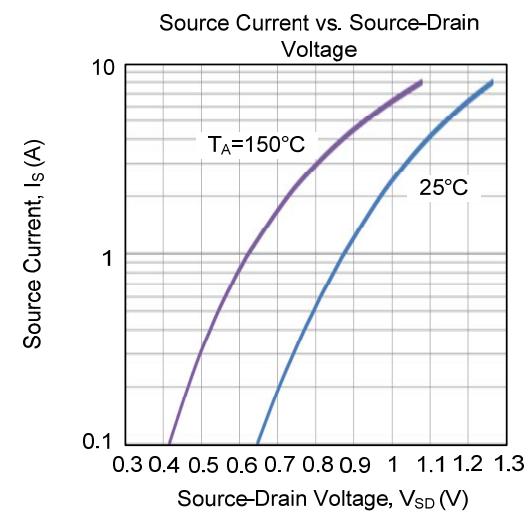
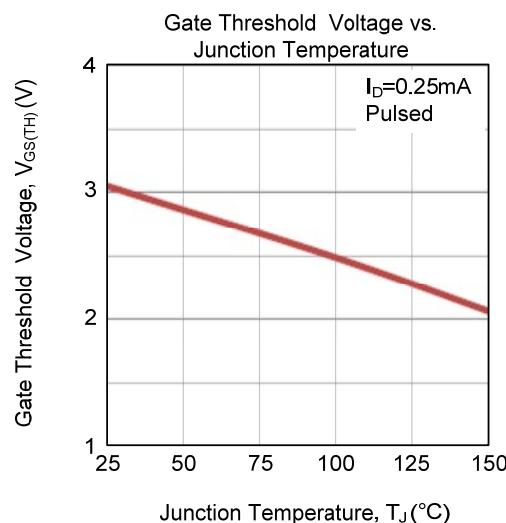
**Unclamped Inductive Switching Test Circuit**

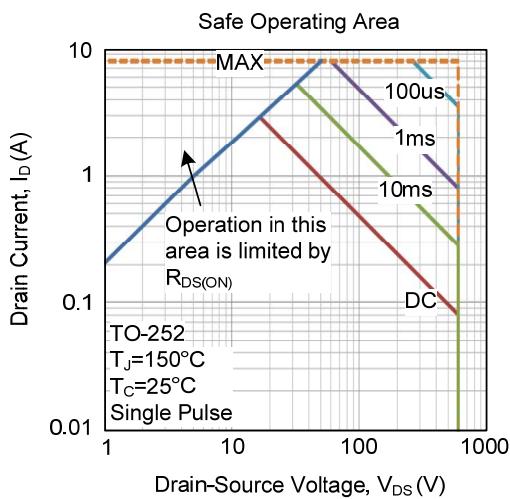
**Unclamped Inductive Switching Waveforms**

■ TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS (Cont.)



**■ TYPICAL CHARACTERISTICS (Cont.)**

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