



## F3N60-LC

Power MOSFET

### 3.0A, 600V N-CHANNEL POWER MOSFET

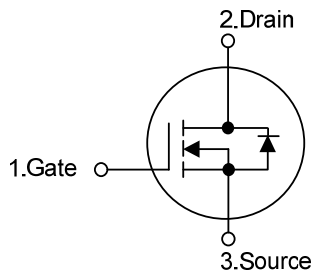
#### DESCRIPTION

The UTC **F3N60-LC** is a N-Channel enhancement mode silicon gate power MOSFET with Fast Body Diode, is designed high voltage, high speed power switching applications such, is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

#### FEATURES

- \*  $R_{DS(ON)} \leq 3.6 \Omega @ V_{GS}=10V, I_D=1.5A$
- \* Fast body diode MOSFET technology
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness

#### SYMBOL

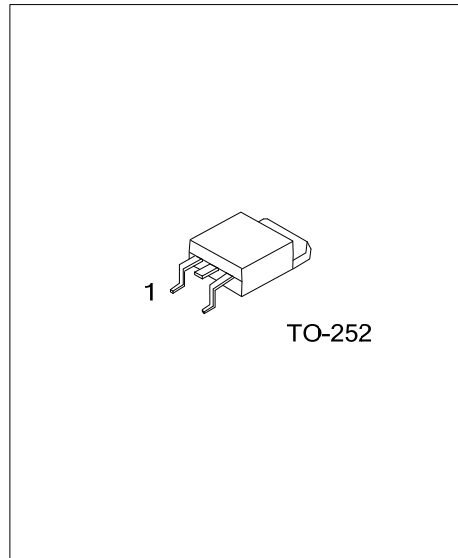


#### ORDERING INFORMATION

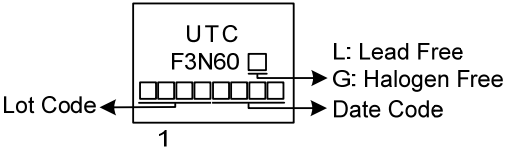
| Ordering Number |              | Package | Pin Assignment |   |   | Packing   |
|-----------------|--------------|---------|----------------|---|---|-----------|
| Lead Free       | Halogen Free |         | 1              | 2 | 3 |           |
| F3N60L-TN3-R    | F3N60G-TN3-R | TO-252  | G              | D | S | Tape Reel |

Note: Pin Assignment: G: Gate D: Drain S: Source

|   |   |
|---|---|
| <p>F3N60G-TN3-R</p> <p>(1)Packing Type<br/>(2)Package Type<br/>(3)Green Package</p> | <p>(1) R: Tape Reel<br/>(2) TN3: TO-252<br/>(3) G: Halogen Free and Lead Free, L: Lead Free</p> |
|---|---|



MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C=25^\circ\text{C}$ , unless otherwise specified)

| PARAMETER                          |                        | SYMBOL    | RATINGS    | UNIT             |
|------------------------------------|------------------------|-----------|------------|------------------|
| Drain-Source Voltage               |                        | $V_{DSS}$ | 600        | V                |
| Gate-Source Voltage                |                        | $V_{GSS}$ | $\pm 30$   | V                |
| Drain Current                      | Continuous             | $I_D$     | 3          | A                |
|                                    | Pulsed (Note 2)        | $I_{DM}$  | 6          | A                |
| Avalanche Energy                   | Single Pulsed (Note 3) | $E_{AS}$  | 92         | mJ               |
| Peak Diode Recovery dv/dt (Note 4) |                        | dv/dt     | 7.28       | V/ns             |
| Power Dissipation                  |                        | $P_D$     | 47         | W                |
| Junction Temperature               |                        | $T_J$     | +150       | $^\circ\text{C}$ |
| Storage Temperature                |                        | $T_{STG}$ | -55 ~ +150 | $^\circ\text{C}$ |

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.  
 2. Repetitive Rating : Pulse width limited by maximum junction temperature.  
 3.  $L=30\text{mH}$ ,  $I_{AS}=2.47\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J = 25^\circ\text{C}$   
 4.  $I_{SD} \leq 3.0\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

| PARAMETER           | SYMBOL        | RATINGS     | UNIT                      |
|---------------------|---------------|-------------|---------------------------|
| Junction to Ambient | $\theta_{JA}$ | 110         | $^\circ\text{C}/\text{W}$ |
| Junction to Case    | $\theta_{JC}$ | 2.66 (Note) | $^\circ\text{C}/\text{W}$ |

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

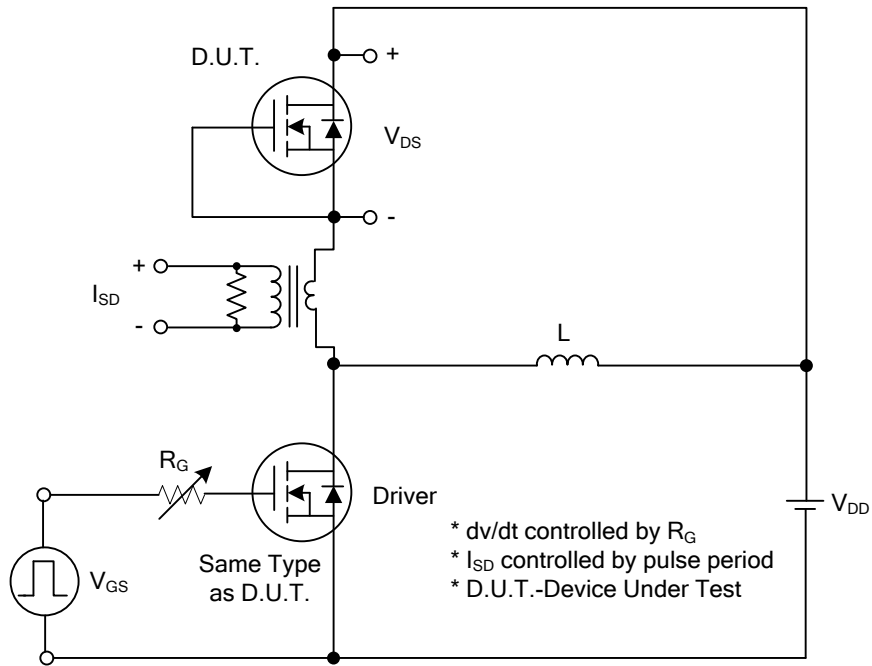
■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

| PARAMETER  | SYMBOL              | TEST CONDITIONS   | MIN | TYP  | MAX  | UNIT |
|--|---------------------|---|-----|------|------|------|
| <b>OFF CHARACTERISTICS</b>                             |                     |   |     |      |      |      |
| Drain-Source Breakdown Voltage                         | BV <sub>DSS</sub>   | V <sub>GS</sub> =0V, I <sub>D</sub> =250μA  | 600 |      |      | V    |
| Drain-Source Leakage Current                           | I <sub>DSS</sub>    | V <sub>DS</sub> =600V, V <sub>GS</sub> =0V  |     |      | 10   | μA   |
| Gate-Source Leakage Current                            | I <sub>GSS</sub>    | V <sub>GS</sub> =30V, V <sub>DS</sub> =0V   |     |      | 100  | nA   |
|  |                     | V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V  |     |      | -100 | nA   |
| <b>ON CHARACTERISTICS</b>                              |                     |   |     |      |      |      |
| Gate Threshold Voltage                                 | V <sub>GS(TH)</sub> | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA  | 2.0 |      | 4.0  | V    |
| Drain to Source On-state Resistance                    | R <sub>DS(ON)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =1.5A  |     |      | 3.6  | Ω    |
| <b>DYNAMIC PARAMETERS</b>                              |                     |   |     |      |      |      |
| Input Capacitance                                      | C <sub>ISS</sub>    | V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz   |     | 430  |      | pF   |
| Output Capacitance                                     | C <sub>OSS</sub>    |   |     | 46   |      | pF   |
| Reverse Transfer Capacitance                           | C <sub>RSS</sub>    |   |     | 4.5  |      | pF   |
| <b>SWITCHING PARAMETERS</b>                            |                     |   |     |      |      |      |
| Total Gate Charge (Note 1)                             | Q <sub>G</sub>      | V <sub>DS</sub> =480V, V <sub>GS</sub> =10V, I <sub>D</sub> =3A,<br>I <sub>G</sub> =1mA (Note 1, 2) |     | 14.8 |      | nC   |
| Gate Source Charge                                     | Q <sub>GS</sub>     |   |     | 5.8  |      | nC   |
| Gate Drain Charge                                      | Q <sub>GD</sub>     |   |     | 2.06 |      | nC   |
| Turn-ON Delay Time (Note 1)                            | t <sub>D(ON)</sub>  | V <sub>DD</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =3A,<br>R <sub>G</sub> =25Ω (Note 1, 2) |     | 6.4  |      | ns   |
| Turn-ON Rise Time                                      | t <sub>R</sub>      |   |     | 16.6 |      | ns   |
| Turn-OFF Delay Time                                    | t <sub>D(OFF)</sub> |   |     | 34   |      | ns   |
| Turn-OFF Fall-Time                                     | t <sub>F</sub>      |   |     | 26   |      | ns   |
| <b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b> |                     |   |     |      |      |      |
| Maximum Continuous Drain-Source Diode Forward Current  | I <sub>S</sub>      |   |     |      | 3    | A    |
| Maximum Pulsed Drain-Source Diode Forward Current      | I <sub>SM</sub>     |   |     |      | 6    | A    |
| Drain-Source Diode Forward Voltage (Note 1)            | V <sub>SD</sub>     | I <sub>S</sub> =3A, V <sub>GS</sub> =0V   |     |      | 1.4  | V    |
| Reverse Recovery Time (Note 1)                         | t <sub>rr</sub>     | I <sub>S</sub> =3A, V <sub>GS</sub> =0V, di/dt=100A/μs  |     | 98   |      | ns   |
| Reverse Recovery Charge                                | Q <sub>rr</sub>     |   |     |      | 414  |      |

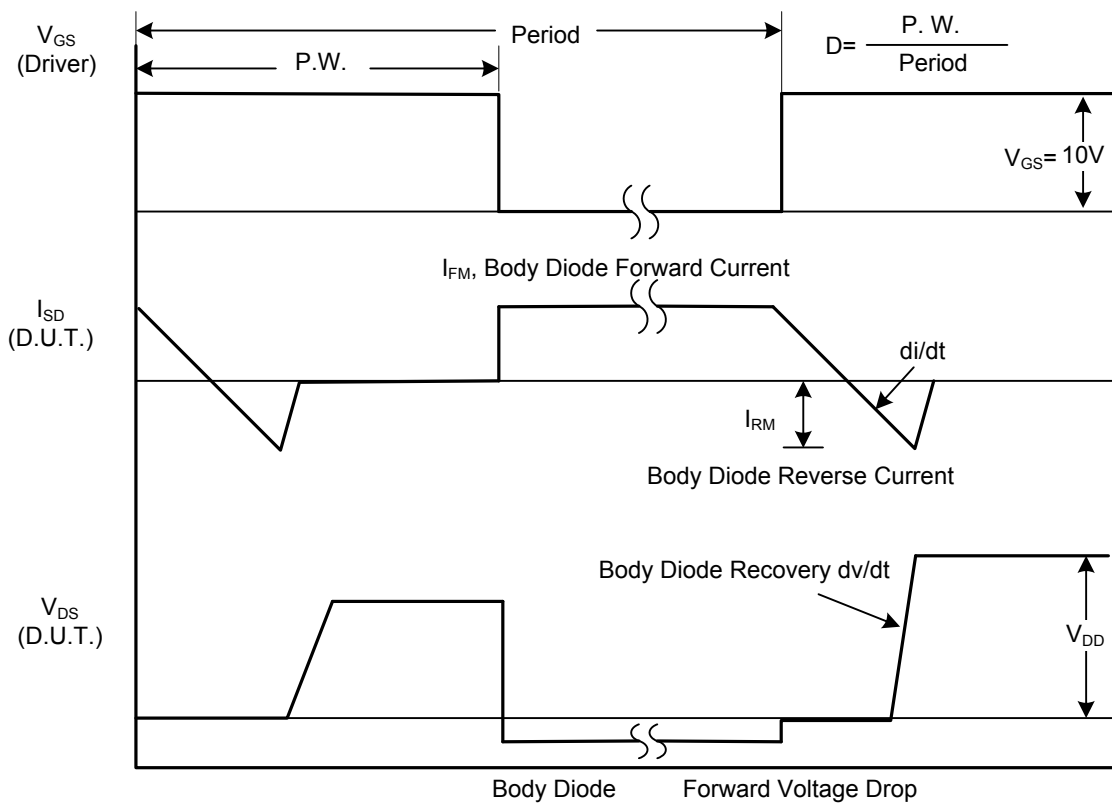
Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

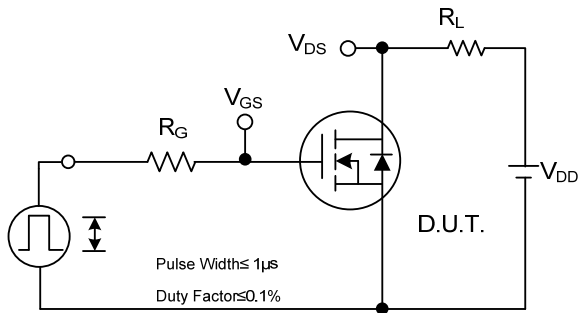


Peak Diode Recovery  $dv/dt$  Test Circuit



Peak Diode Recovery  $dv/dt$  Waveforms

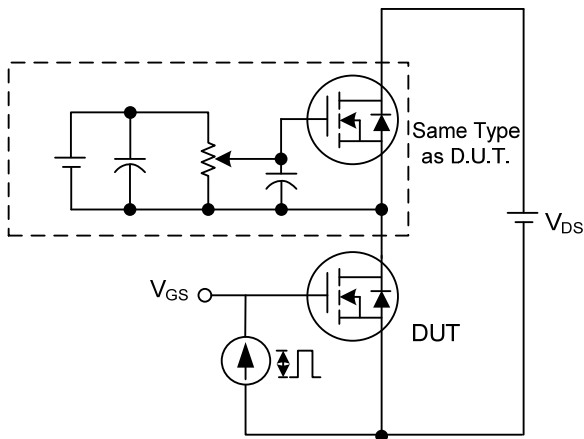
## TEST CIRCUITS AND WAVEFORMS



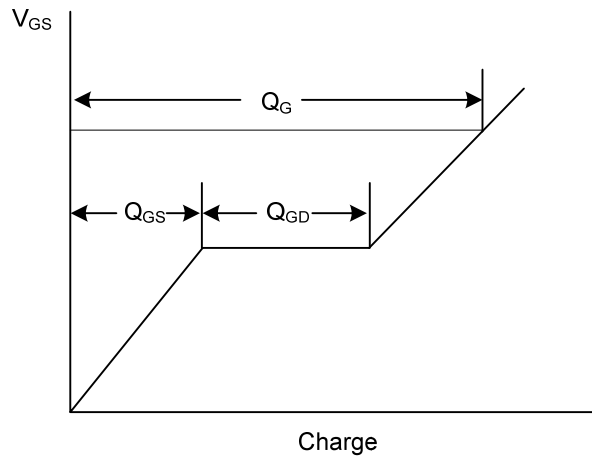
**Switching Test Circuit**



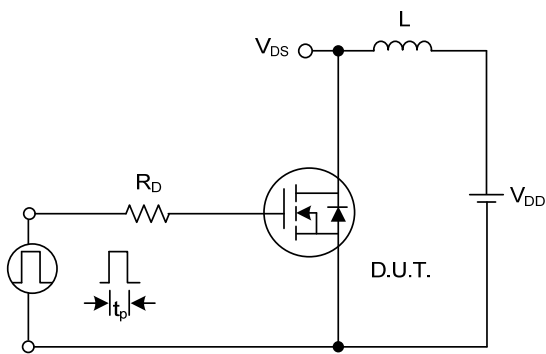
**Switching Waveforms**



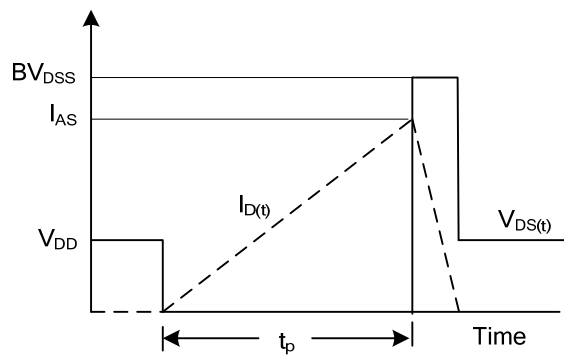
**Gate Charge Test Circuit**



**Gate Charge Waveform**

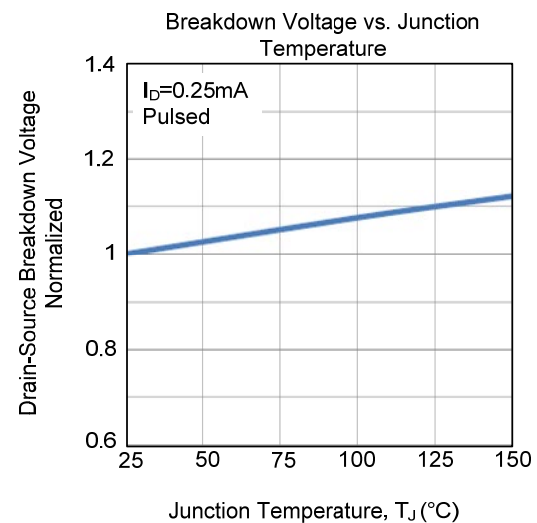
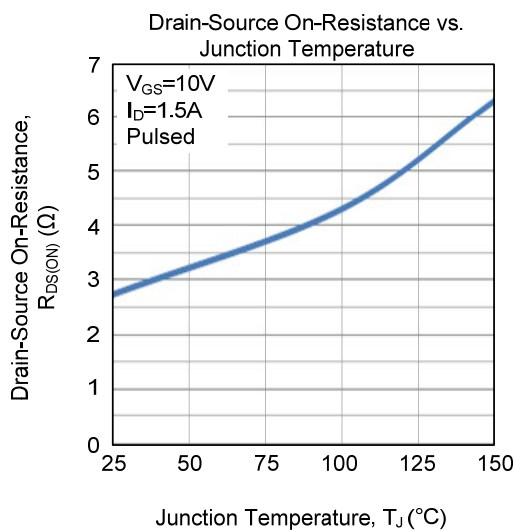
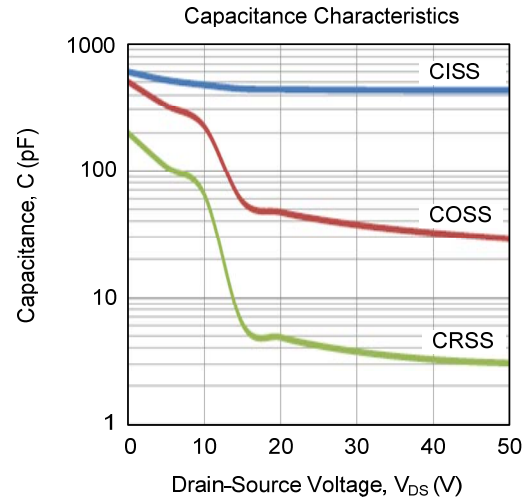
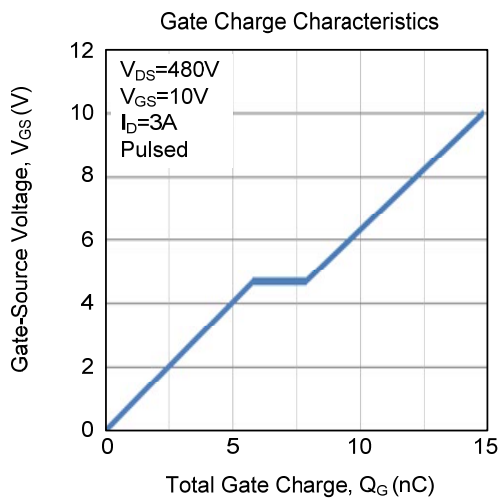
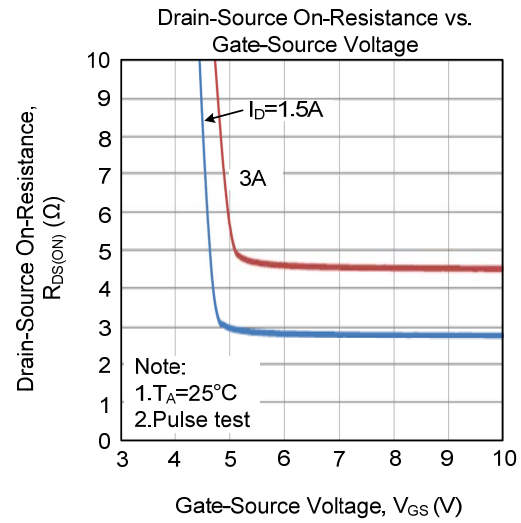
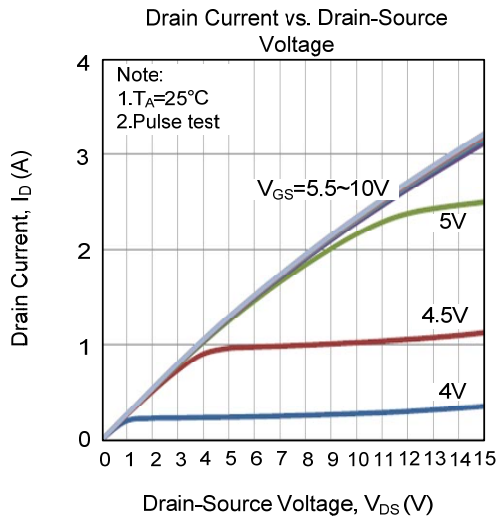


**Unclamped Inductive Switching Test Circuit**

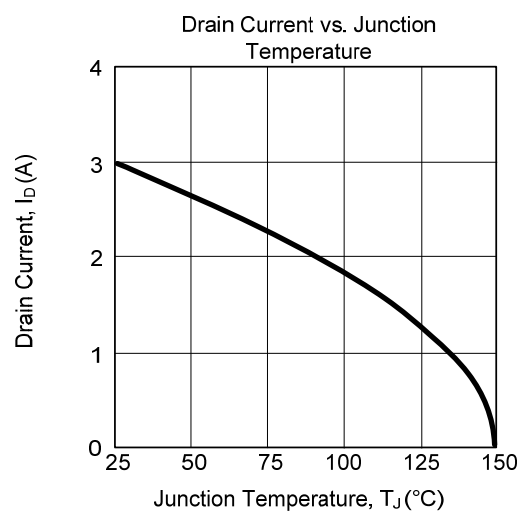
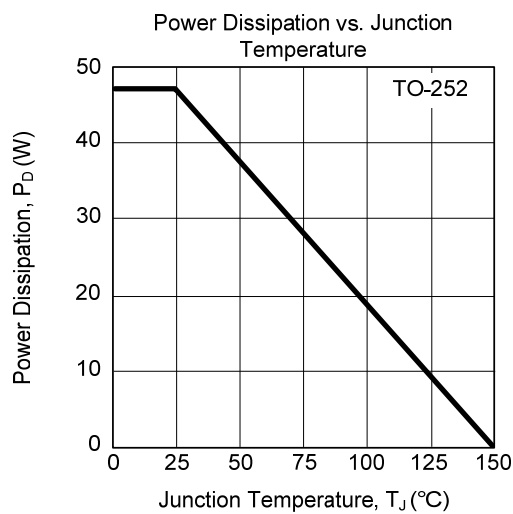
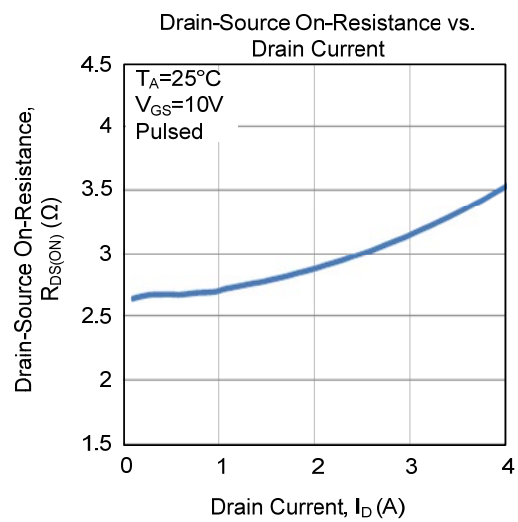
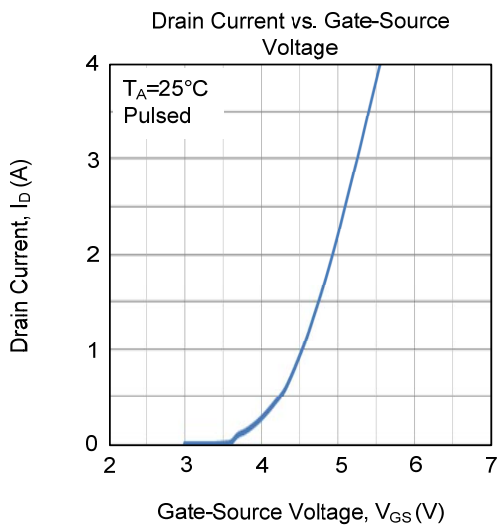
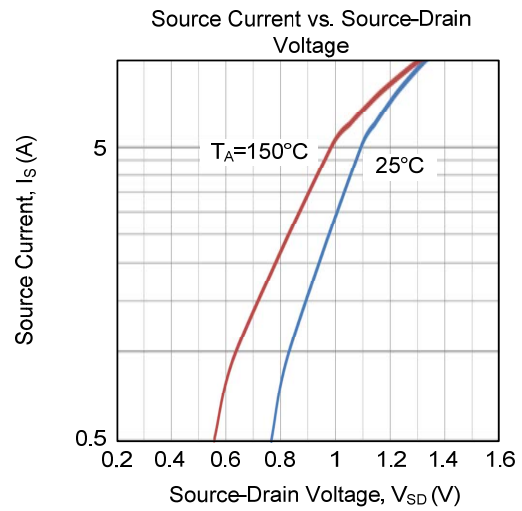
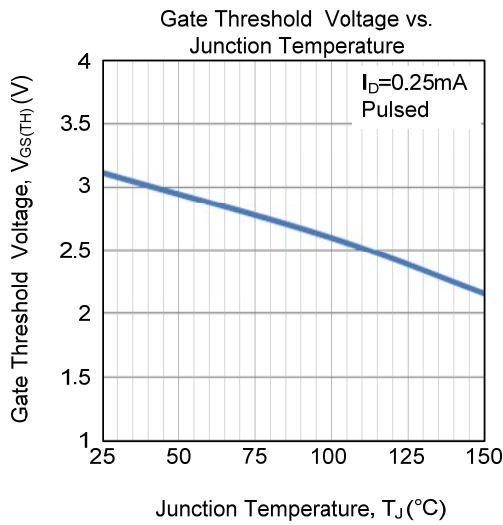


**Unclamped Inductive Switching Waveforms**

## TYPICAL CHARACTERISTICS

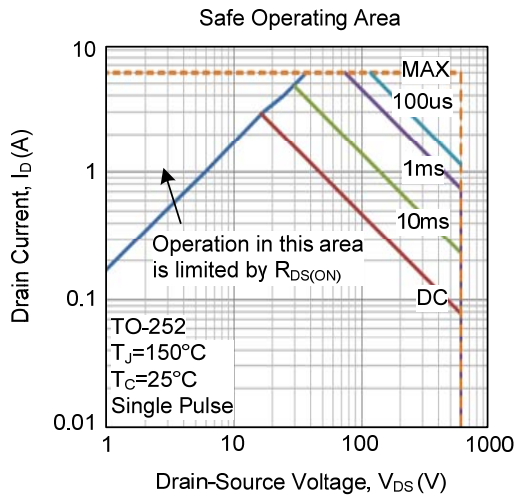


## ■ TYPICAL CHARACTERISTICS (Cont.)





■ TYPICAL CHARACTERISTICS (Cont.)



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