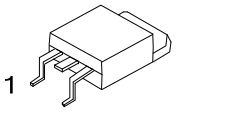
0.5A, 500V N-CHANNEL POWER MOSFET



TO-252

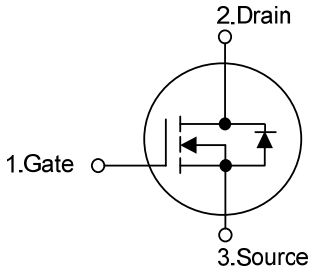
■ DESCRIPTION

The UTC **F05N50-TD** is a N-Channel enhancement mode silicon gate power MOSFET with Fast Body Diode, is designed high voltage, high speed power switching applications such, is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

■ FEATURES

* $R_{DS(ON)} \leq 11.4\ \Omega$ @ V_{GS}=10V, I_D=0.25A
* Fast body diode MOSFET technology
* Fast switching capability
* Avalanche energy tested
* Improved dv/dt capability, high ruggedness

■ SYMBOL



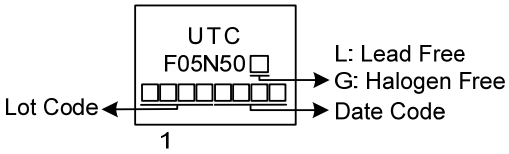
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
F05N50L-TN3-R	F05N50G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

F05N50G-TN3-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	0.5	A
	Pulsed (Note 2)	I_{DM}	1	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	18	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	6	V/ns
Power Dissipation		P_D	28	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating : Pulse width limited by maximum junction temperature.
3. L=30mH, I_{AS}=1.1A, V_{DD}=90V, R_G=25Ω, Starting T_J = 25°C
4. I_{SD} ≤ 0.5A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	110	°C/W
Junction to Case	θ_{Jc}	4.46	°C/W

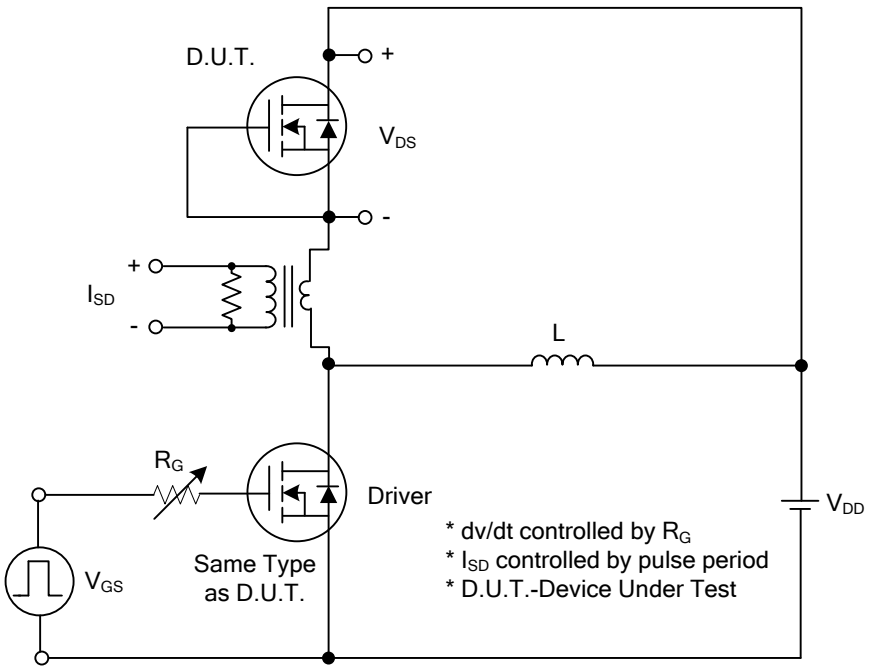
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

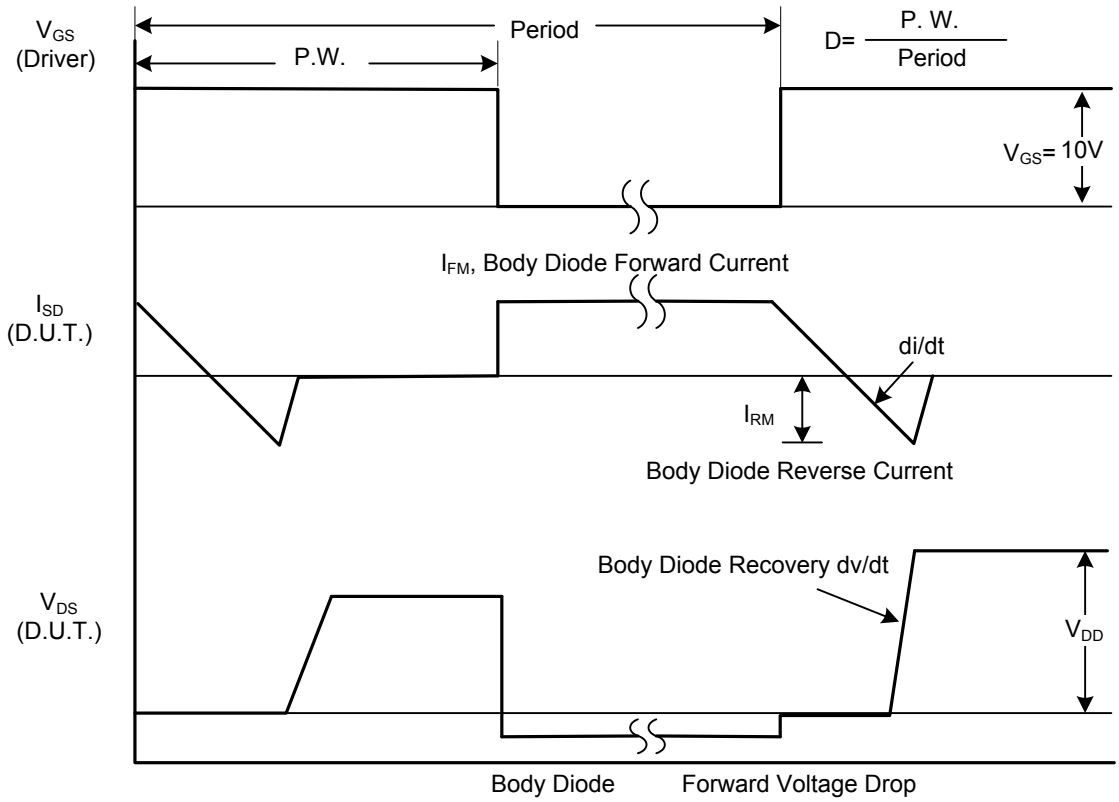
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250μA	500			V
Drain-Source Leakage Current	I_{DSS}	V_{DS} =500V,V_{GS} =0V			10	μA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=30V, V_{DS}=0V			100	nA
		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Drain to Source On-state Resistance	$R_{DS(ON)}$	V_{GS} =10V, I_D =0.25A			11.4	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		91		pF
Output Capacitance	C_{OSS}			15		pF
Reverse Transfer Capacitance	C_{RSS}			2.1		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q_G	V_{DS}=400V, V_{GS}=10V, I_D=0.5A, I_G=1mA (Note 1, 2)		3.7		nC
Gate Source Charge	Q_{GS}			1.7		nC
Gate Drain Charge	Q_{GD}			0.6		nC
Turn-ON Delay Time (Note 1)	$t_{D(ON)}$	V_{DD}=100V, V_{GS}=10V, I_D=0.5A, R_G =25Ω (Note 1, 2)		5		ns
Turn-ON Rise Time	t_R			15		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			12		ns
Turn-OFF Fall-Time	t_F			31		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I_S				0.5	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				1	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	I_S =0.5A, V_{GS} =0V			1.4	V
Reverse Recovery Time (Note 1)	t_{rr}	I_S=0.5A,V_{GS}=0V, dI/dt=100A/μs		57		ns
Reverse Recovery Charge	Q_{rr}			80		nC

Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

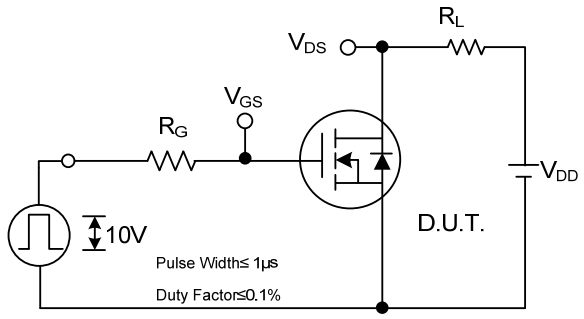


Peak Diode Recovery dv/dt Test Circuit

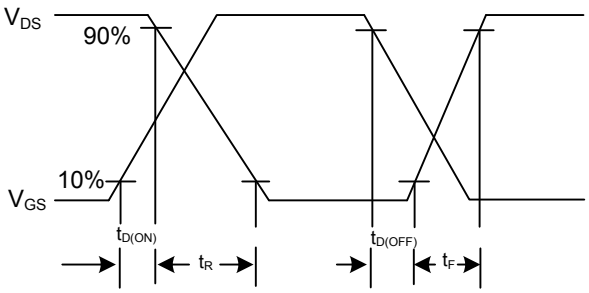


Peak Diode Recovery dv/dt Waveforms

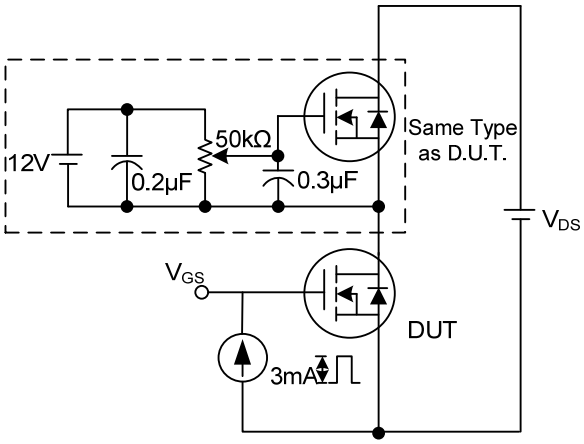
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



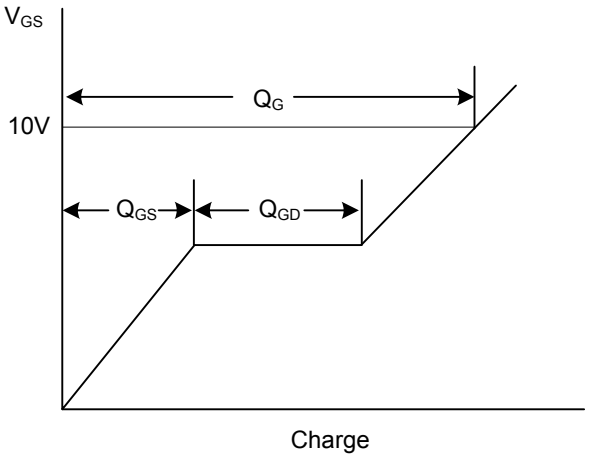
Switching Test Circuit



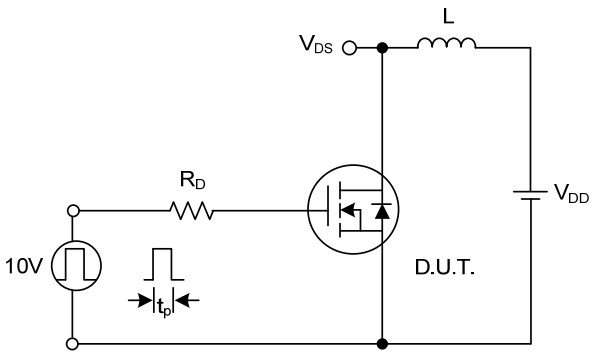
Switching Waveforms



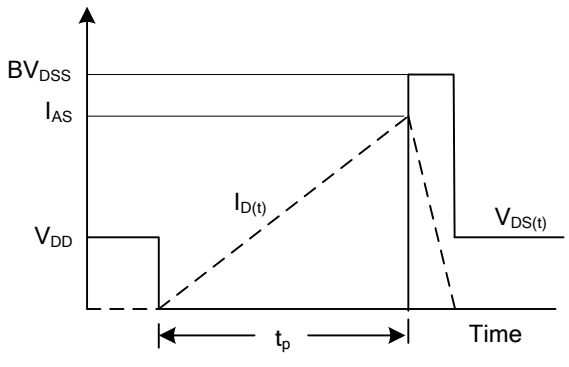
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms